

FACULTY PROFILE

Faculty Name: Dr. A. Mallikarjuna Prasad

Faculty Photo:



Faculty Description:

Dr. A. Mallikarjuna Prasad is the Associate Professor, Department of Electrical and Electronics Engineering at Dr.K.V.Subba Reddy Institute of Technology, Kurnool.

Profile Tab:

Qualification

Qualification	Institution	Year
Bachelor's degree	University of Madras, Chennai	2001
Master's degree	Satyabama Deemed University, Chennai.TN	2004
.Ph.D	Annamalai University, Chithambaram,TN.	2014

Experience

Designation	Institution	From	To
Assistant Professor	St. Johns College of Engineering & Technology, Yemmiganur	2004	2008
Associate Professor	Bheema College of Engineering & Technology, Adoni	2008	2010
Associate Professor	Srinivasa College of Engineering & Technology, Kurnool	2010	2012
Associate Professor	St. Johns College of Engineering & Technology, Yemmiganur.	2012	2017
Associate Professor	Dr. K. V. Subba Reddy Institute of Technology	2017	Till date

Awards / Achievement

- Ratified as Assistant Professor in 2005 at JNTUH, Hyderabad.(Stood First place)
- Attend as a resource person for Technical Symposium Dr. SGIT, Markapuram in 2017
- Convener for two days national level workshop on robotics and mobile making organized by IIT Mumbai at KVSRIIR kurnool in 2019 OCT.

Responsibilities Tab:

Responsibilities

- Project Coordinator
- Department Lab Incharge
- Convener of Anti ragging committee
- Convener of Student Alumni committee.

Responsibilities Held

- HOD of EEE Dept from July 2003 to 2016
- As a JNTUA University Question paper setter in 2017 to 2019.
- In charge for the Online Internal Examinations conducted by JNTU Hyderabad.
- In charge projects for B.Tech
- Handled projects for M.Tech
- In charge of Academic Plans and Time Tabe

Teaching Tab:

list of subjects handled

- ❖ Power Electronics.
- ❖ Power semiconductor Drives.
- ❖ HVDC
- ❖ Control Systems.
- ❖ Analysis of Linear Systems.
- ❖ Advanced Control Systems.
- ❖ Network Theory.
- ❖ Basic Electrical Engineering.

- **Research Tab:**

Research Interest

- Power converters fed DC drives

- Power System Stability
- FLC and ANN control in closed loop system

Publications tab:

1. A. Mallikarjuna Prasad, D.Tulasi Manasa, B.Prudvi Kumar Reddy, “FOUR QUADRANT OPERATION AND CONTROL OF THREEPHASEBLDC MOTOR FOR ELECTRIC VEHICLES” in International Journal of Food and Nutritional Sciences, 2021.
2. A. Mallikarjuna Prasad, K. Mahesh, D.Tulasi Manasa, “GRID-BASED VARIABLE SPEED WIND ENERGY CONVERSION SYSTEM POWER QUALITY IMPROVEMENT”, Material Science and Technology, Vol.20 No.05, 2021.
3. A. Mallikarjuna Prasad, K. Mahesh M. Madhusudhan Reddy, “SMART GRID POWER QUALITY IMPROVEMENT USING MODIFIED UPQC”, POSITIF Journal Vol 22, Issue 12 2022.
4. K. Rajesh, A. Mallikarjuna Prasad,S. Masum Basha, “ELECTRIC VEHICLE REGENERATIVE BRAKING POWERED BY A BLDC MOTOR”, Material Science and Technology Vol.22 No.08 2023.

Workshops/Seminars/Book Authors.

- Two Days National Level workshops “Embedded Systems and Wireless Technologies” in SJCET, Yemmiganur.
- Two days National Level Workshops “Recent Trends in Power Electronics Drives” in GPREC, Kurnool.
- Power Semiconductor Drives - S.Siva Nagaraju, A.Mallikarjuna Prasad & S. Balasubba Reddy by PHI Private Limited, Publications First Edition 2008 - New Delhi.
- HVDC Systems - S.Siva Nagaraju, A.Mallikarjuna Prasad & S. Balasubba Reddy by Redge, Publications First Edition 2008 - Hyderabad.
- Power Electronics - S.Siva Nagaraju, A.Mallikarjuna Prasad & S. Balasubba Reddy by PHI Private Limited, Publications Under Printing, New Delhi.

FDPs and STTPs Tab :

1. Participated in a One-week National Level Online Faculty Development Programme on “Recent Trends in Green Energy Initiatives and Soft Computing Techniques” from 11-07-2023 to 17-07-2023 organized by the Department of Electrical and Electronics Engineering, Mahatma Gandhi Institute of Technology.

2. Participated in the Training program/FDP on “FIRST HAND EXPERIENCE ON SPSS – INTRODUCTORY MODULE” Organised by faculty of commerce - Parul University, Research foundation of India & RFI care from 06 November to 12 November 2022

3. Completed all requirements of an AICTE Sponsored One Week National Level Online STTP on “Recent Trend And Challenges In Power Market With Smart Grid Technology” (PHASE-III) Organise and Electronics Engineering, 20/09/2021 TO 25/09/2021.

